

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
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PATENT APPLICATION

ATTORNEY DOCKET NO. 10990672-3



IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Daniel Y. Ambramovitch

Confirmation No.: 7876

Application No.: 10/696,993

Examiner: Vuong, Bach Q

Filing Date: 10/30/2003

Group Art Unit: 2653

Title: Harmonic Correction in Phase-Locked Loops

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

TERMINAL DISCLAIMER
RESPONSIVE TO A DOUBLE PATENTING REJECTION

Sir:

Petitioner, Hewlett-Packard Development Company, L.P. is the owner of 100 percent interest in the instant application. Petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 to 156 and 173 as shortened by any terminal disclaimer of prior Patent No. 6,646,964 to Hewlett-Packard Development Company, L.P. which issued on 11/11/2003 and is commonly owned by Petitioner. Petitioner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

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Please charge the required fee set forth in 37 CFR 1.20(d) of \$110.00 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees.

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Respectfully submitted,

Daniel Y. Ambramovitch

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Date: Sept. 14 2004

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